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Docket No.: M4065.0468/P468  
(PATENT)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:  
Phillip E. Byrd, et al.

Application No.: 09/941,761

Allowed: May 20, 2003

Filed: August 30, 2001

Group Art Unit: 2812

For: METHOD AND STRUCTURE FOR  
TEMPORARILY ISOLATING A DIE  
FROM A COMMON CONDUCTOR TO  
FACILITATE WAFER LEVEL TESTING

Examiner: A. Stevenson

**REQUEST FOR COMPLETE NOTICE OF ALLOWANCE**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

In reviewing the above-captioned application file upon allowance, Applicant's undersigned representative has noticed that Applicant's Form PTO-1449, which was filed on January 8, 2002, has not yet been acknowledged by the Examiner. A copy of the PTO date-stamped post card and Form PTO-1449 is enclosed. The Examiner is therefore kindly requested to return the initialed form to the undersigned as soon as possible.

Dated: June 18, 2003

Respectfully submitted,

By 

Thomas J. D'Amico

Registration No.: 28,371

Christopher A. Monsey

Registration No.: 53,342

DICKSTEIN SHAPIRO MORIN &  
OSHINSKY LLP

2101 L Street NW

Washington, DC 20037-1526

(202) 785-9700

TECHNOLOGY CENTER 2800

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